

side

set

*DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ*

<u>L4</u>	L2 and (predefin\$3 or predetermin\$3) near4 bus near3 transaction	24	<u>L4</u>
<u>L3</u>	L2 and phase near2 generator and (predefin\$3 or predetermin\$3) near4 bus near3 transaction	0	<u>L3</u>
<u>L2</u>	L1 and (predetermin\$3 or predefin\$3) (instruction or command) near3 (memory or stor\$3) and (plurality or multiple or another)near4 (instruction or command) and transaction same bus and (sequence or simultaneous\$2)	1040	<u>L2</u>
<u>L1</u>		1619	<u>L1</u>

END OF SEARCH HISTORY